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APPLICANTS

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IA

** CONTINUING DATA *****
 This application is a DIV of 09/413,594 07/02/2001 PAT 6,765,286 *
 (*)Data provided by applicant is not consistent with PTO records.

IA

** FOREIGN APPLICATIONS *****
 JAPAN P10-297162 10/19/1998
 JAPAN P11-026519 02/03/1999

IF REQUIRED, FOREIGN FILING LICENSE GRANTED
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Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 6	TOTAL CLAIMS 2	INDEPENDENT CLAIMS 2
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance Verified and Acknowledged <i>[Signature]</i> Examiner's Signature	<i>IA</i> Initials			

ADDRESS
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TITLE
 Semiconductor integrated circuit card manufacturing method, and semiconductor integrated circuit card

FILING FEE RECEIVED 770	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue)
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